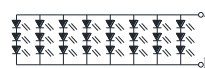
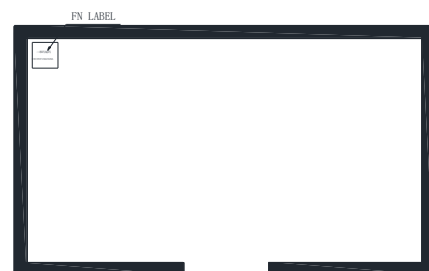
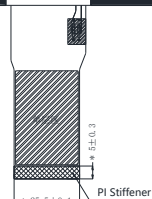


± 2.69±0.3
0.05 (CF POL到通孔距离)



Backlight LED Circuit
LED CIRCUIT DIAGRAM:
BACKLIGHT: 3SSP=24 CHIP-WHITE LED
IF=160mA ; VF=8.1~10.2V



± 2.69±0.03 (FPC+PI Stiffener)

GENERAL INFORMATION		
Item	Contents	Unit
LCD Size	7.0	Inch
Number Of Pixels	1024×600	---
Pixel pitch (W x H)	0.0502(H)×0.1432(V)	mm
Active Area	157.4(H)×88.7(V)	mm
Viewing area (W x H)	162.8×62.22	mm
LCM outline (W x H x T)	163.86(W)×97.06(H)×2.69(D)	mm
Display mode	IPS,NB	---
View angle (L/R/U/D)	70/70/70/70	Degree
TFT Driver IC	HX8282&HX8696	---
Interface Type	RGB	---
Color depth	/	M
LCM brightness	MIN 320	Cd/m ²
With/Without tp	/	---
TFT Power consumption	40	mw
BL Power consumption	1440	mw
Operation Temperature	-20~70	°C
LED life time	30,000 (TYP)	Hrs

INTERFACE DESCRIPTION			
No.	SYMBOL	I/O	Description
1-2	LEDA	P	Power for LED backlight(Anode)
3-4	LEDK	P	Power for LED backlight(Cathode)
5	GND	P	Ground.
6	VCOM	I	Common electrode voltage input
7	VDD	P	Power Supply (+3.3V)
8	MODE	I	DE/SYNC mode select. Normally pull high. H: DE mode, L: HSD/VSD mode.
9	DE	I	Data enable singal.
10	VSD	I	Vertical sync input. Negative polarity.
11	HSD	I	Horizontal sync input. Negative polarity.
12-19	B7-B0	I	Blue data
20-27	G7-G0	I	Green data
28-35	R7-R0	I	Red data
36	GND	P	Ground
37	DCLK	I	Clock Signal Input.
38	GND	P	Ground
39	SHLR	I	Left or right display control
40	UPDN	I	Up/down display control
41	VGH	I	Positive power for TFT
42	VGL	I	Negative power for TFT
43	AVDD	P	Analog Power Supply
44	RSTB	I	Global reset pin. Active low to enter reset state. Normally pull high.
45	NC	-	No connection
46	VCOM	I	Common electrode voltage input
47	DITH	I	Dithering setting . DITH="H" 6bit resolution (last 2 bit of input data truncated) DITH="L" 8bit resolution (default setting)
48	GND	P	Ground.
49	NC	-	No connection
50	NC	-	No connection

ELECTRICAL CHARACTERISTICS					
Parameter	Symbol	Min.	Typ.	Max.	Unit
Power Supply Voltage	VDD	2.3	3.3	3.6	V
	AVDD	-	9.6	-	V
	VGH	-	18	-	V
	VGL	-	-6	-	V
	VCOM	3.1	3.2	3.3	V
Power Supply current	IDD	-	12.0	-	mA
Input logic high voltage	VIH	0.7VDD	-	VDD	v
Input logic low voltage	VIL	0	-	0.3VDD	v

BACKLIGHT DRIVING CONDITION					
Item	Symbol	Min	Typ.	Max	Unit
Forward current	If	--	--	160	mA
Forward voltage	Vf	8.1	9.0	10.2	V